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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/010,343	12/05/2001	Visvamohan Yegnashankaran	072219-0261615 (P05089)	7286

33402 7590 02/27/2003

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EXAMINER

ANDUJAR, LEONARDO

ART UNIT	PAPER NUMBER
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2826

DATE MAILED: 02/27/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/010,343

Applicant(s)

YEGNASHANKARAN ET AL.

Examiner

Leonardo Andújar

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 28 January 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) 4-20 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-3 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 05 December 2001 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 2) ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ 6) ☐ Other: _____

DETAILED ACTION

Election/Restrictions

1. Applicant's election without traverse of Group I (claims 1-3) in Paper No. 8 is acknowledged.

Drawings

2. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, "*a second die having a micro mechanical structure having inductance which further includes a micro-electromechanical structure having capacitance according to claim 2*" must be shown or the feature(s) canceled from the claim(s). No new matter should be entered. A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 112

3. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

4. Claim 2 is rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. The specification does not contain any disclosure regarding "a semiconductor package having a plurality of fourth bonding pads". Note that the specification does not disclose that the "package

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having a plurality of fourth bonding pads" or multi-layer substrate 210 is made of semiconductor.

5. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

6. Claim 3 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

7. Claim 3 recites the limitation "a micro-electromechanical structure" in line 2. It is not clear if this structure is a second micro-electromechanical structure or it is the same micro-electromechanical structure recited in claim 1.

Claim Rejections - 35 USC § 103

8. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

9. Claims 1-3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Wark (US 6,399,416) in view of Wolf.

10. Regarding claim 1, Wark (e.g. attached figure 2) shows an integrated circuit comprising:

➤ A first die 26 having:

- a. A substrate with an electrical circuit (col. 4/lls. 27-35);
- b. A plurality of first bonding pads 26a;

- c. A plurality of second bonding pads 26b;
- A second die 32;
 - a. A micro electro-mechanical structure having inductance;
 - b. A plurality of third bonding pads 32a connected to the micro electro-mechanical structure;
 - c. A plurality of connectors 32b electrically connected to the second bonding pads and the third bonding pad;

11. Wark does not explicitly teach that the IC chip 26 includes interconnects formed on the substrate and electrically connected to the electrical circuit, and a passivation layer. Nonetheless, it is well known in the art that IC chips include interconnects on the semiconductor substrate as well as bonding pads formed on a passivation layer. Wolf teaches that metal interconnections (one or more level) are formed on the substrate to interconnect circuit elements and to carry large amounts of supply current. Moreover, passivation layers are used to protect the device from contaminants, moisture and scratches (pages 826-829). It would have been obvious to one of ordinary skill in the art at the time the invention was made to form interconnects on Wark's substrate in order to interconnect the circuit elements as taught by Wolf. Also, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form the plurality of first and second bonding pads disclosed by Wark on a passivation layer in order to protect the device from contaminants, moisture and scratches as taught by Wolf.

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12. Regarding claim 2 (as understood), Wark discloses a semiconductor package 12 having a plurality of fourth bonding pads, a plurality of circuit boards connectors, and internal routing (fig. 3b, col. 4/lls. 46-54). Also, Wark discloses a plurality of wires 26c connected to the first bonding pads and the fourth bonding pads.

13. Regarding claim 3 (as understood), Wark discloses that the second die discloses include a micro electromechanical structure that has capacitance (col. 4/lls. 27-35, col. 5/lls. 20-27).

Conclusion

14. Papers related to this application may be submitted directly to Art Unit 2826 by facsimile transmission. Papers should be faxed to Art Unit 2826 via the Art Unit 2826 Fax Center located in Crystal Plaza 4, room 3C23. The faxing of such papers must conform to the notice published in the Official Gazette, 1096 OG 30 (15 November 1989). The Art Unit 2826 Fax Center number is **(703) 308-7722** or **-7724**. The Art Unit 2826 Fax Center is to be used only for papers related to Art Unit 2826 applications.

15. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Leonardo Andújar** at **(703) 308-0080** and between the hours of 9:00 AM to 7:30 PM (Eastern Standard Time) Monday through Thursday or by e-mail via Leonardo.Andujar@uspto.gov. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn, can be reached on (703) 308-6601.

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16. Any inquiry of a general nature or relating to the status of this application should be directed to the **Group 2800 Receptionist** at **(703) 305-3900**.

17. The following list is the Examiner's field of search for the present Office Action:

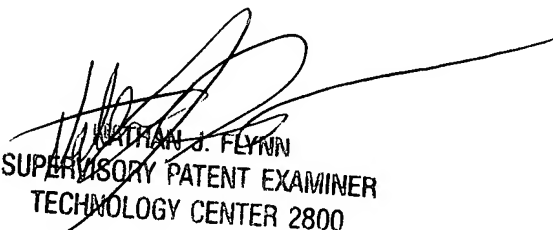
Field of Search	Date
U.S. Class / Subclass (es): 257/777	02/03
Other Documentation:	
Electronic Database(s): East (USPAT, US PG PUB, JPO, EPO, Derwent, IBM TDB)	02/03

Leonardo Andújar

Patent Examiner Art Unit 2826

LA

2/18/03


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